U.S. PATENT DOCUMENTS

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	A	US-6,339,837	01-2002	Li, Zhe	716/5
	B	US-6,389,374	05-2002	Jain et al.	703/2
	C	US-5,642,304	06-1997	Simpson, John Richard	708/446
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	U	"Logic Verification using Binary Decision Diagrams in a Logic Synthesis Environment" by Sharad Malik et al., 1988 IEEE, pages 6-9.
	V	"Boolean Functions Classification via Fixed Polarity Reed-Muller Forms" by Chien-Chung Tsai et al., 1997 IEEE TRANSACTIONS ON COMPUTERS, Vol 46, No. 2, pages 173-186.
	W	The Computer Science and Engineering Handbook, by Allen B. Tucker, CRC Press, ISBN: 0-8493-2909-4, 1996, pages 287, 288, 2034.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.